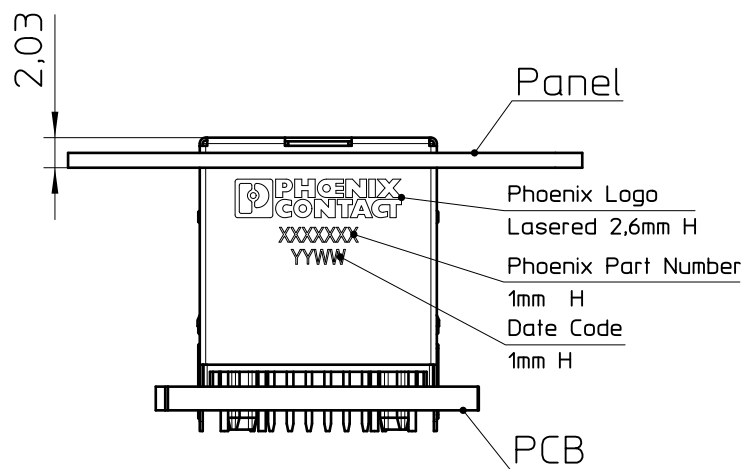
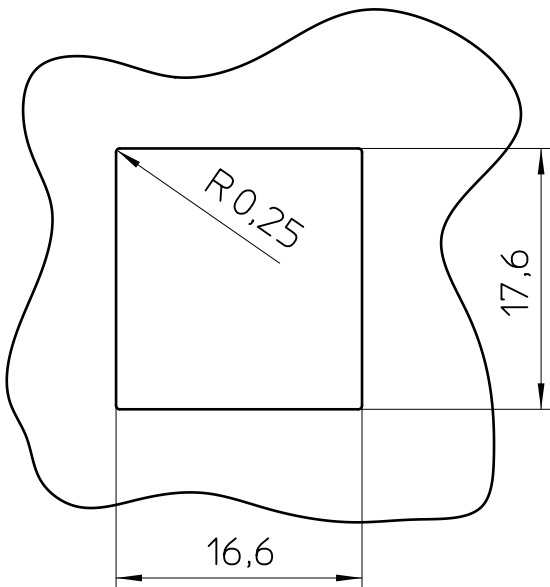


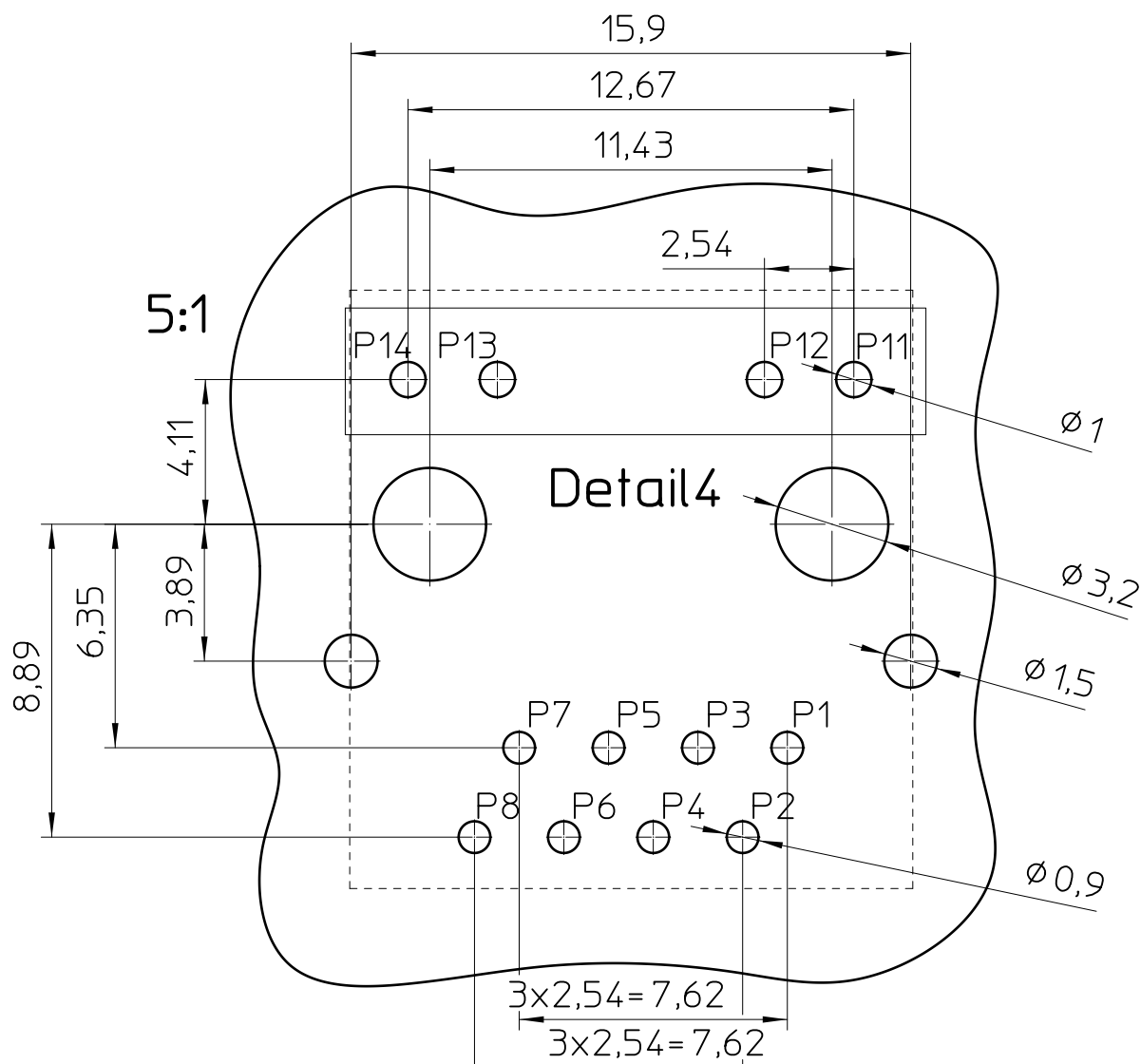
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Recomended panel cutout

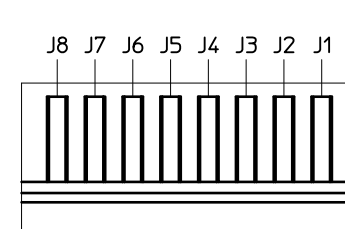
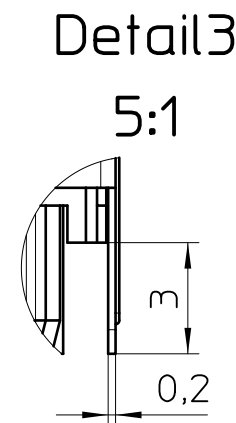
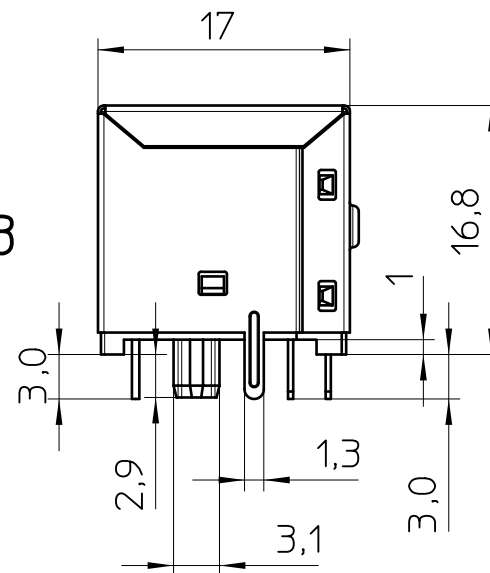
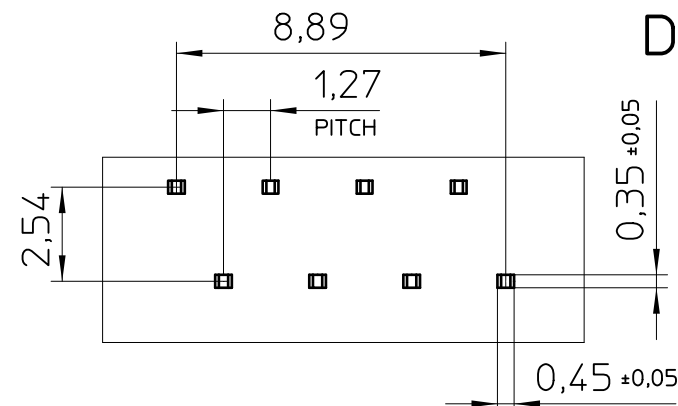
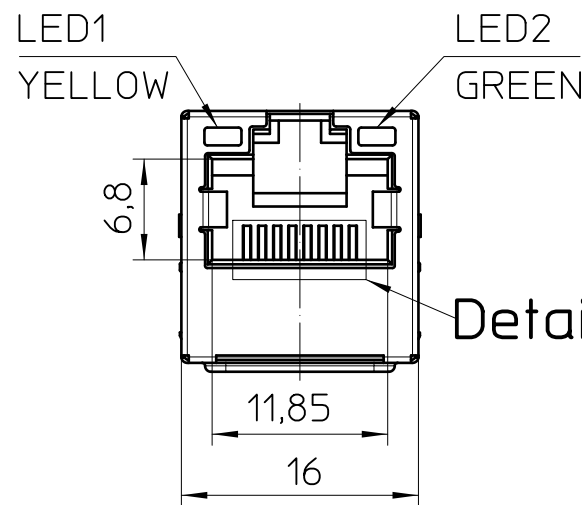
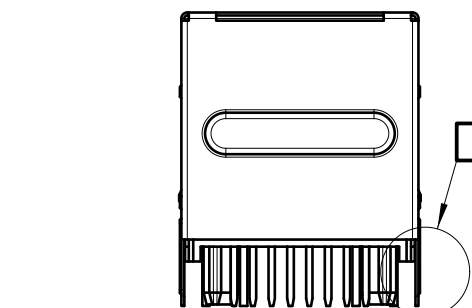
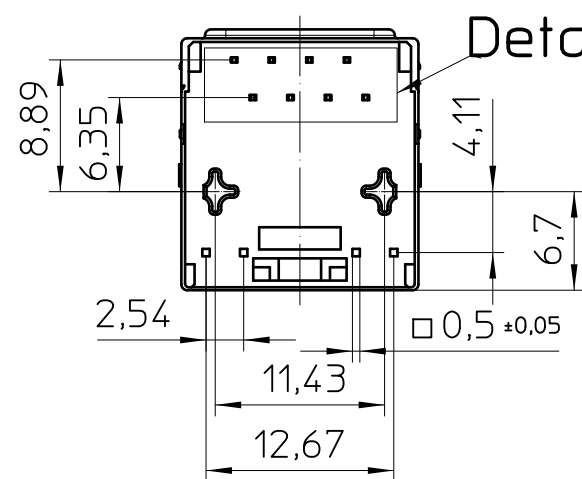


PCB drill hole dimensions (see from component side)



Recommended PCB Layout (Tol: ±0,05, T=1,60±0,15mm)

PCB thickness: 1,6mm



LED 1
YELLOW
14  13

LED 2
GREEN
12  11

LED-Specifications		
LED	Forward Voltage (max)	*Forward Voltage (Typical)
GREEN	2,4 V	2,2 V
YELLOW	2,5 V	2,1 V

*Forward DC current: 20 mA

General Tolerances

1 Decimal Place	±0,25
2 Decimal Places	±0,20
3 Decimal Places	±0,15

 DIN A3



page 1 of 2

document-no. / ri
01760923 / 01

article-no.
1663977

date
12.12.2024

document-type
TECDOC 2D_Productfamily

description
CUC-IC3M-J1ST-S/R4T-L1

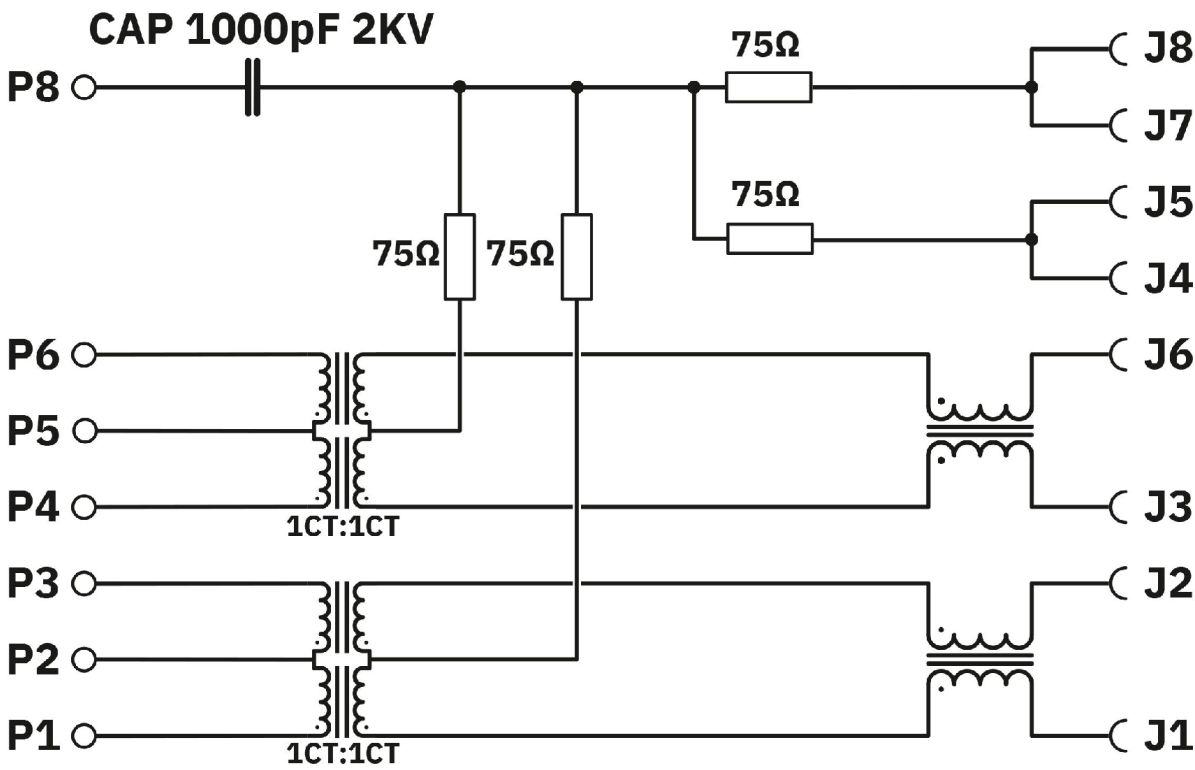
scale
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- Simplified representation
- Document excluded
from change process
- Linear dimensions [mm]

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PCB SIDE



JACK SIDE

ELECTRONICAL SPECIFICATIONS

1. ACCORDING TO IEEE 802.3
2. TURN RATIO @100KHz: (P1~P3):(J1~J2) = 1:1±5%
(P4~P6):(J3~J6) = 1:1±5%
3. PRIMARY INDUCTANCE: 350μH MIN @100KHz
0,1V 8mA DC BIAS
4. DC RESISTANCE: 1,2 OHMS MAX
5. INSERTION LOSS: 1-100MHz -1,2dB MAX
6. RETURN LOSS: 1-30MHz -16dB MIN
30-60MHz -12dB MIN
60-80MHz -10dB MIN
7. CROSS TALK: 1-100MHz -30dB MIN
8. COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN
9. ISOLATION: PHY SIDE TO LINE SIDE: 1500VAC or 2250VDC

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		date 12.12.2024	document-type TECDOC 2D_Productfamily			
page 2 of 2		description CUC-IC3M-J1ST-S/R4T-L1				

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