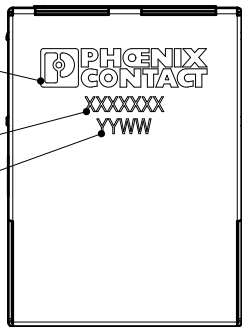
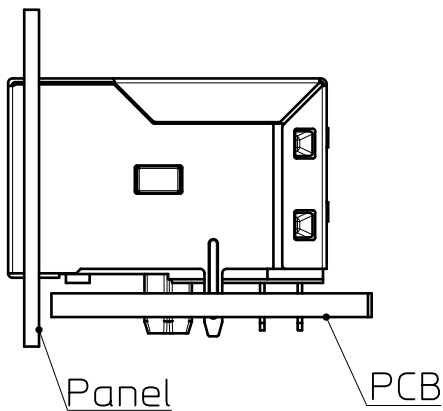
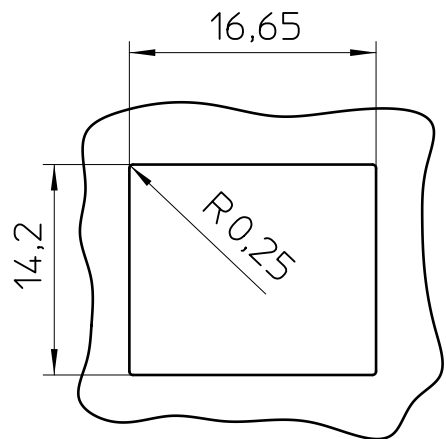


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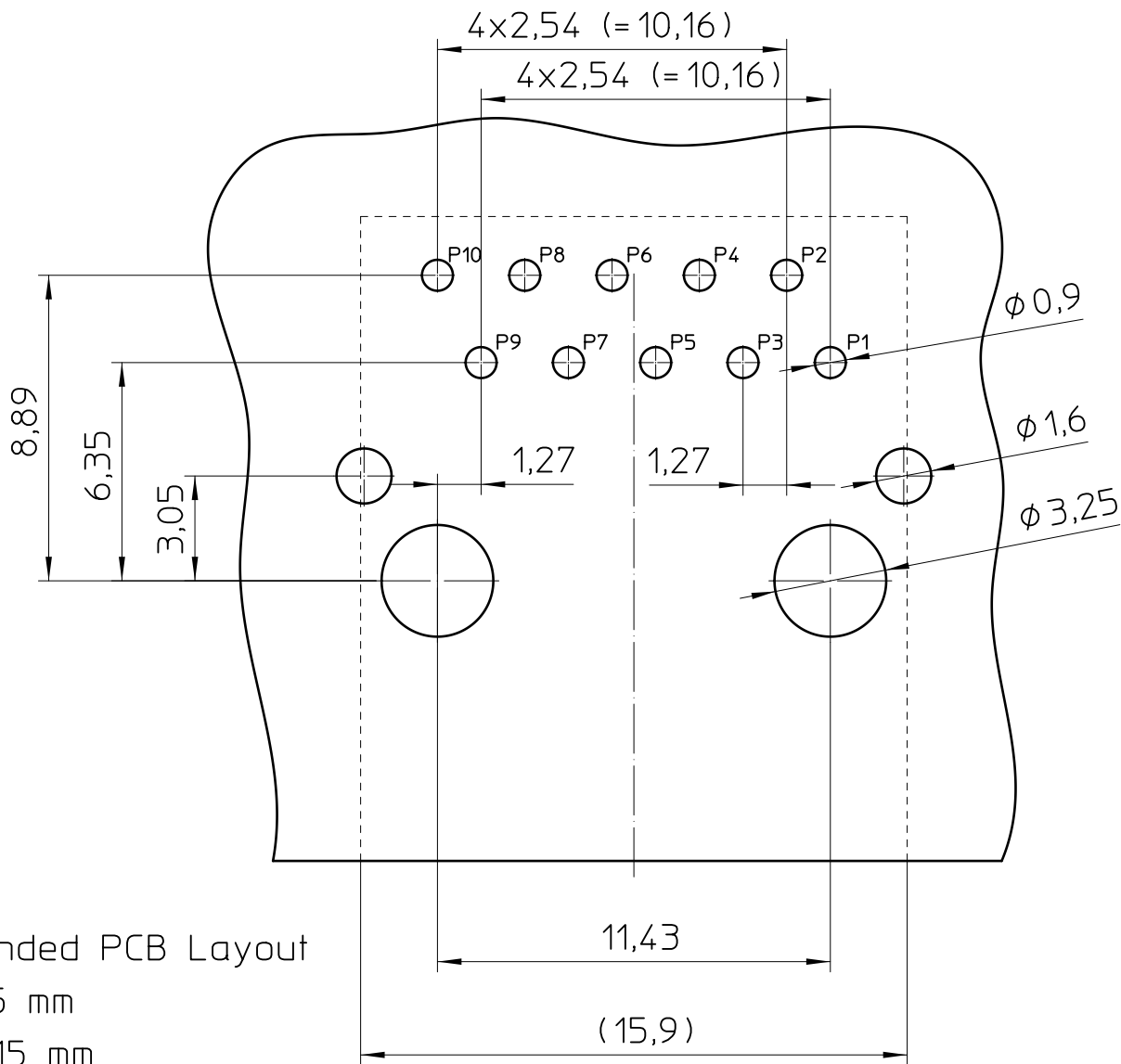
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Recommended panel cutout



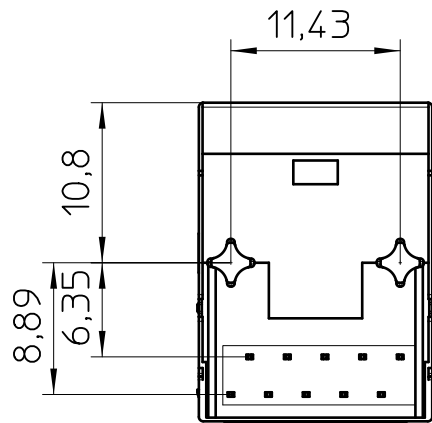
Phoenix Logo
Lasered 2,6 mm H
Phoenix Part Number
1 mm H
Date Code
1 mm H

PCB drill hole dimensions (see from component side)

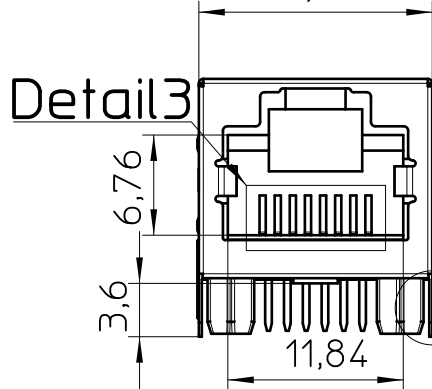


Recommended PCB Layout

Tol: $\pm 0,05$ mm
T: $1,60 \pm 0,15$ mm

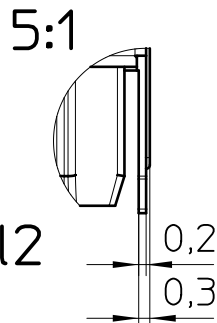


Detail1
15,75



Detail3

Detail2

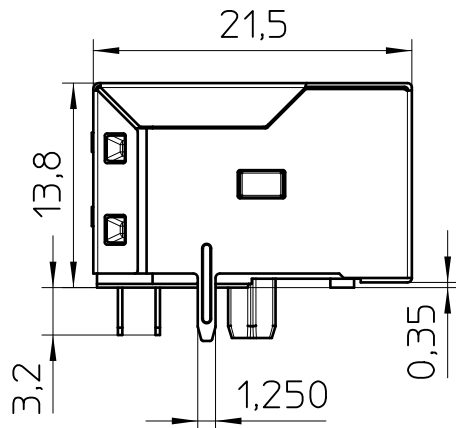
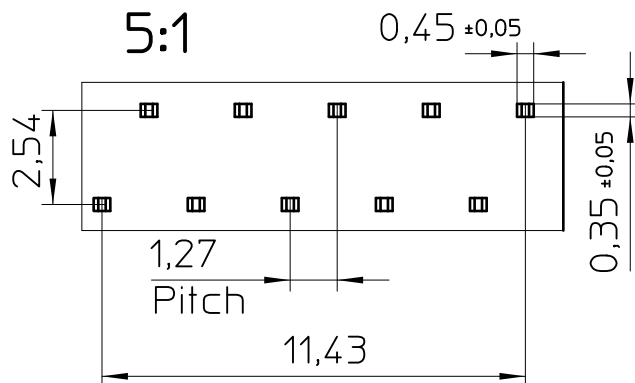


Detail3
5:1



Detail1

5:1



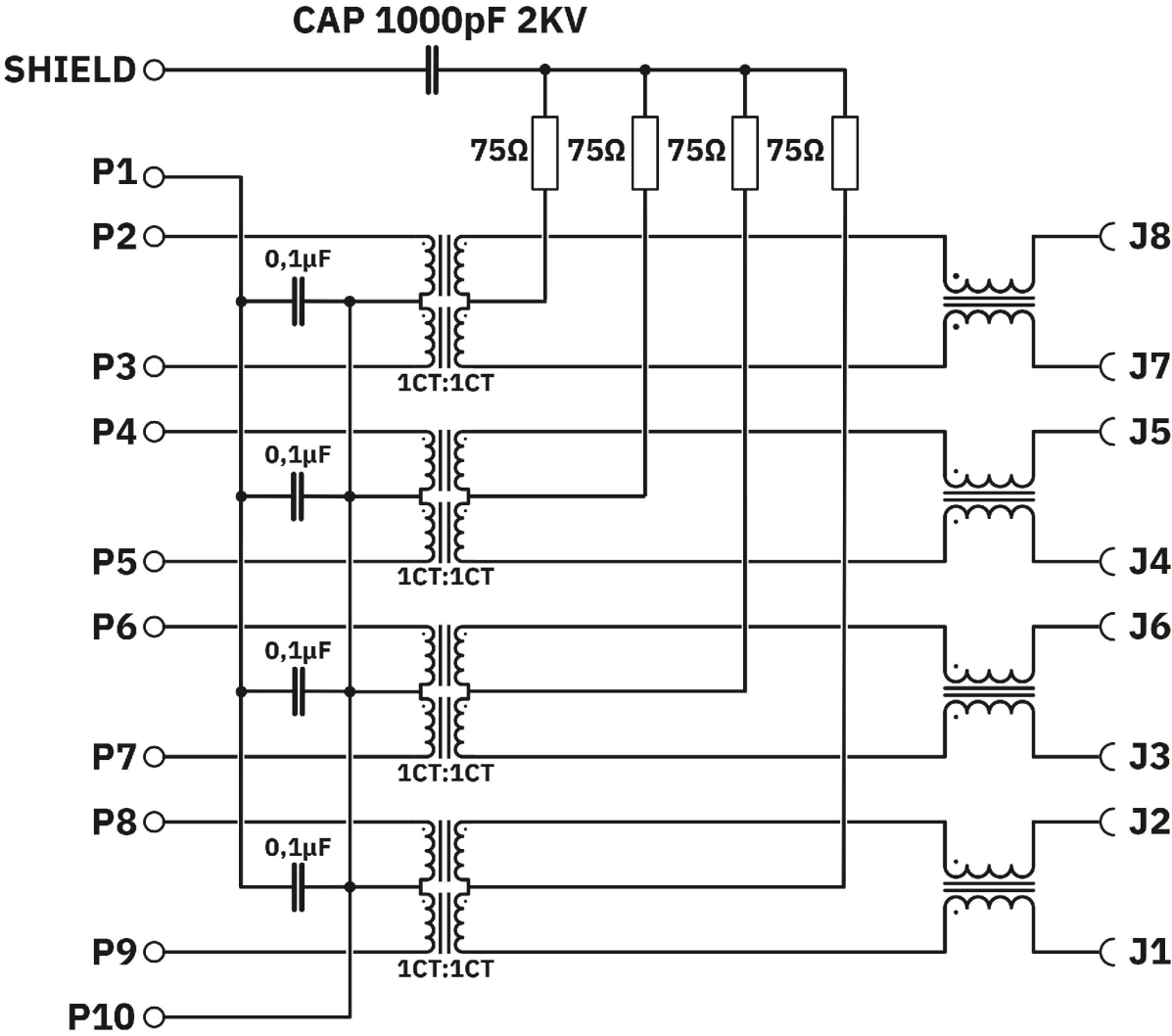
General Tolerances		 DIN A3	document-no. / ri	article-no.	scale	- Simplified representation - Document excluded from change process - Linear dimensions [mm]
1 Decimal Place	±0,25		date	document-type		
2 Decimal Places	±0,20		07.11.2024	TECDOC 2D_Productfamily		
3 Decimal Places	±0,15	page 1	of 2	description		
				CUC-IB1G-J1ST-AT/R4T		

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PCB SIDE

JACK SIDE



ELECTRONICAL SPECIFICATIONS

1. ACCORDING TO IEEE 802.3
2. TURN RATIO @100KHz: (P2~P3):(J1~J2) = 1:1±5%
(P4~P5):(J3~J6) = 1:1±5%
(P6~P7):(J4~J5) = 1:1±5%
(P8~P9):(J7~J8) = 1:1±5%
3. PRIMARY INDUCTANCE: 350μH MIN @100KHz
0,1V 8mA DC BIAS
4. DC RESISTANCE: 1,2 OHMS MAX
5. INSERTION LOSS: 1-100MHz -1,0dB MAX
100-125MHz -1,2dB MAX
6. RETURN LOSS: 1-40MHz -16dB MIN
40-60MHz -12dB MIN
60-80MHz -10dB MIN
80-100MHz -8dB MIN
7. CROSS TALK: 1-100MHz -30dB MIN
8. COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN
9. ISOLATION: PHY SIDE TO LINE SIDE: 2250VDC

- EN ISO 291 23/50

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description CUC-IB1G-J1ST-AT/R4T					
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