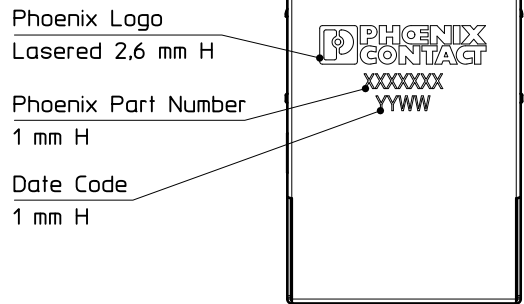
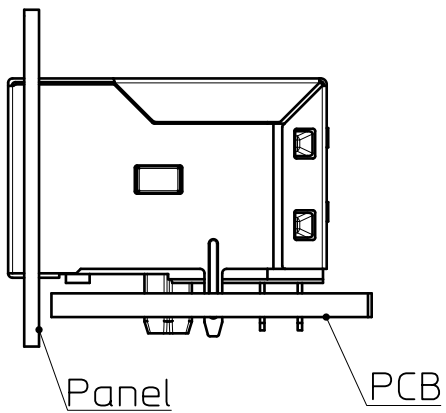
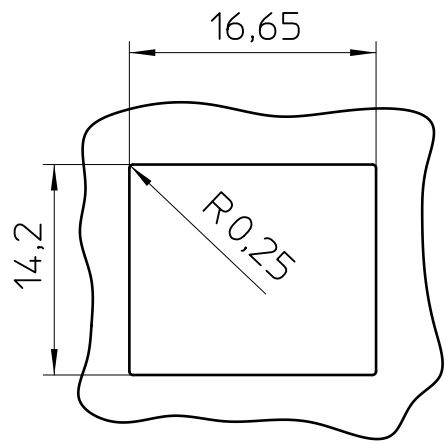


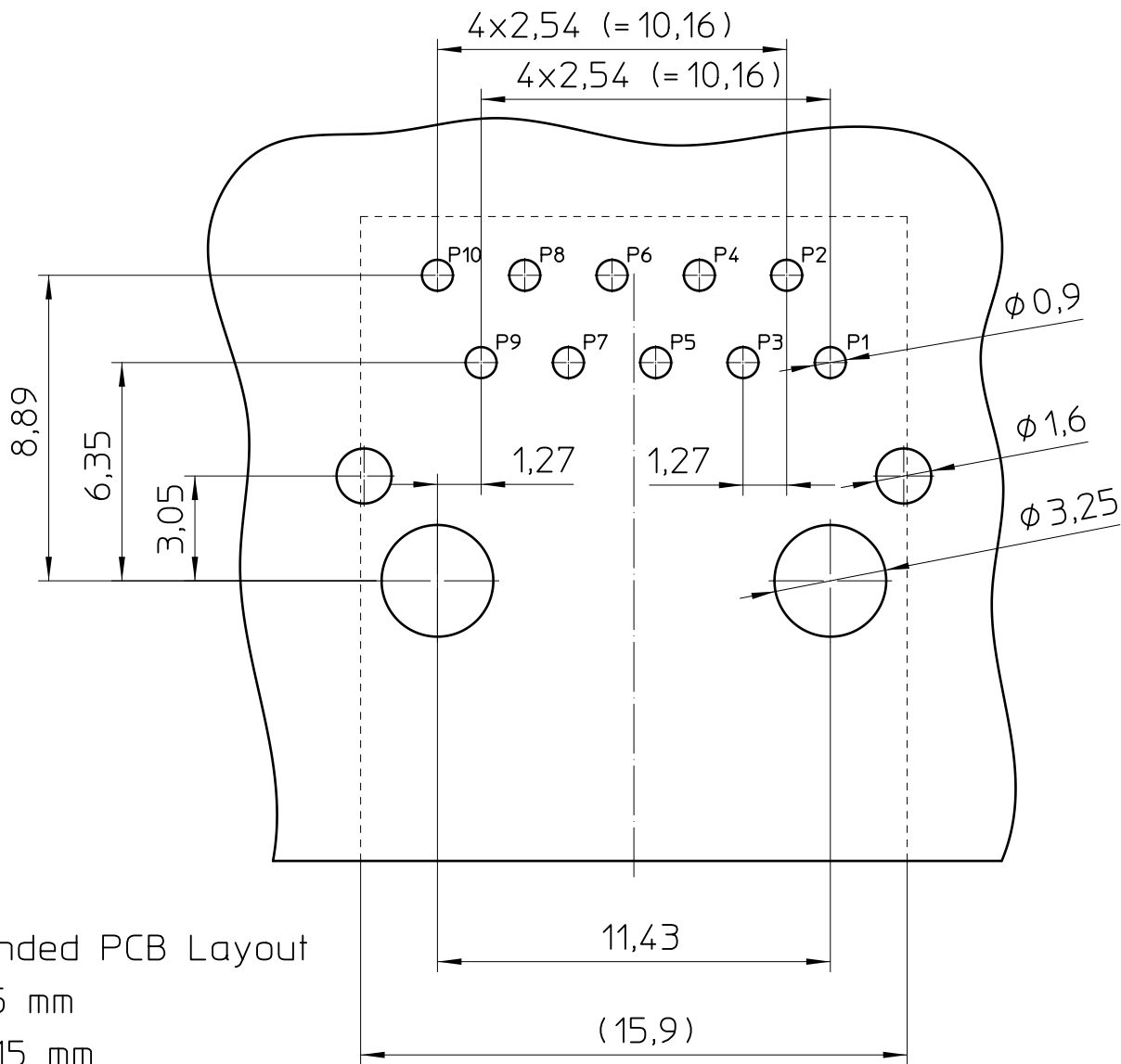
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Recommended panel cutout



PCB drill hole dimensions (see from component side)



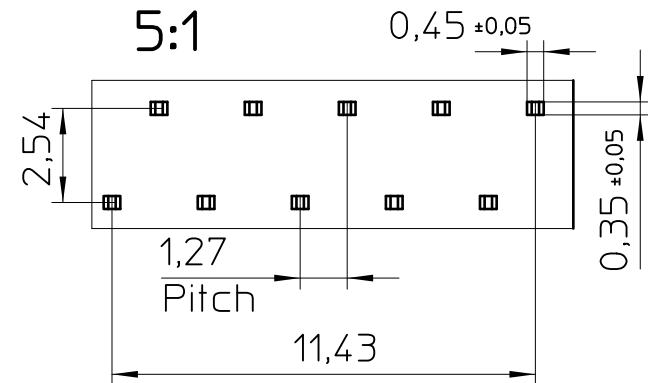
Recommended PCB Layout

Tol: $\pm 0,05$ mm

T=1,60 $\pm 0,15$ mm

Detail1

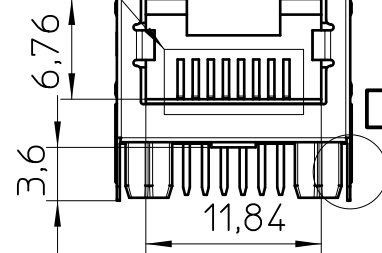
5:1



Detail1

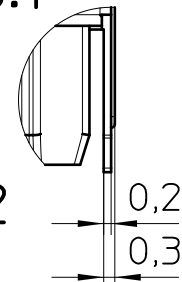
15,75

Detail3



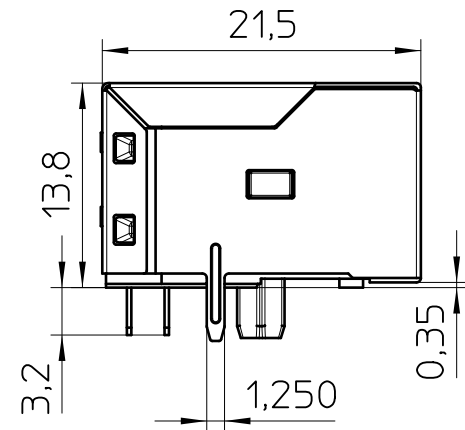
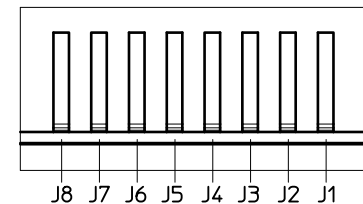
Detail2

5:1



Detail3

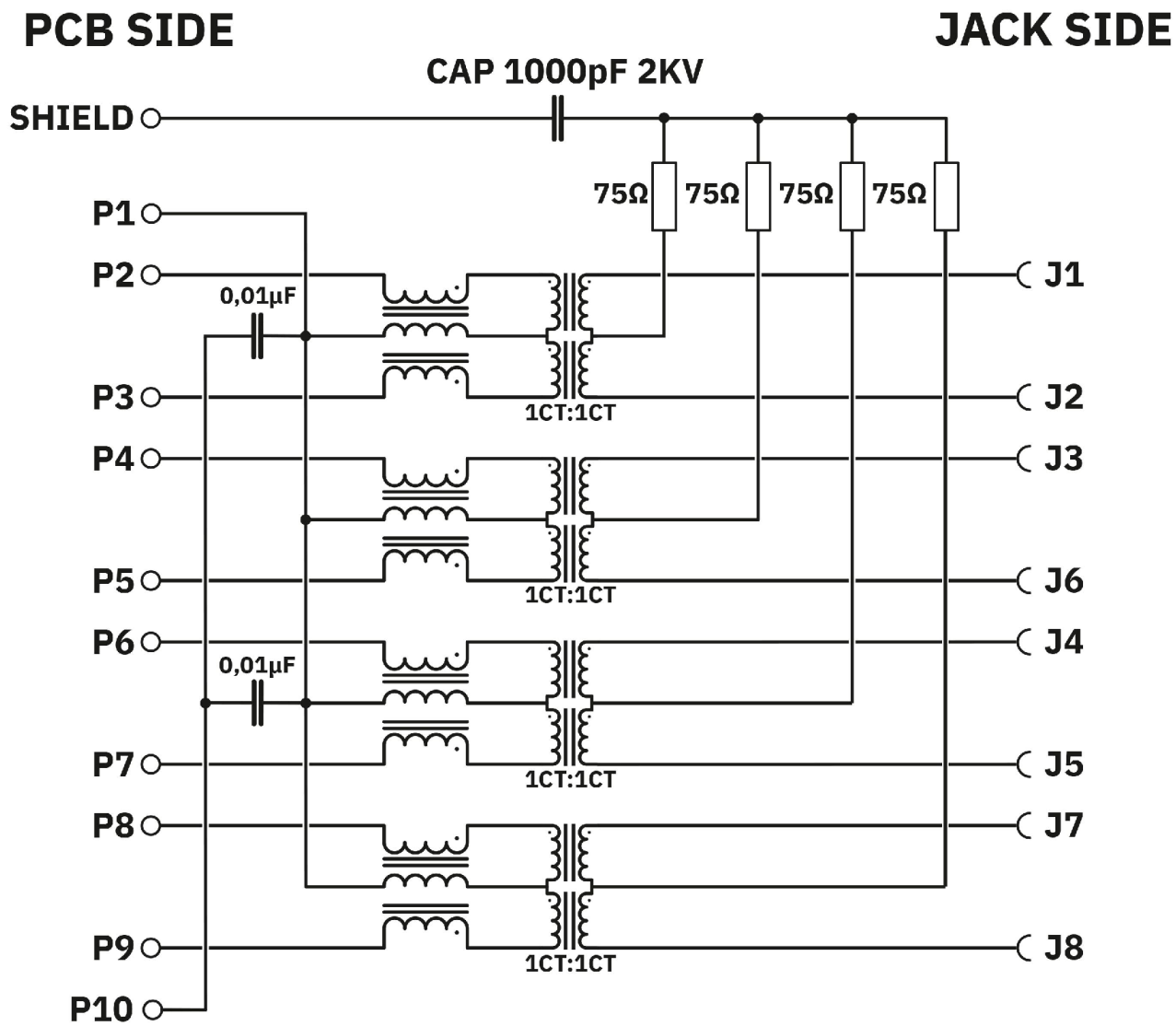
5:1



General Tolerances		 DIN A3		document-no. / ri 01759664 / 00		article-no. 1664025		scale 2:1		- Simplified representation - Document excluded from change process - Linear dimensions [mm]
1 Decimal Place ±0,25				date 07.11.2024		document-type TECDOC 2D_Productfamily				
2 Decimal Places ±0,20				description CUC-IB8G-J1ST-AT/R4T						
3 Decimal Places ±0,15		page 1						of 2		

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ELECTRONICAL SPECIFICATIONS

1. ACCORDING TO IEEE 802.3

2. TURN RATIO @100KHz: (P2~P3):(J1~J2) = **1:1±5%**
(P4~P5):(J3~J6) = **1:1±5%**
(P6~P7):(J4~J5) = **1:1±5%**
(P8~P9):(J7~J8) = **1:1±5%**

3. PRIMARY INDUCTANCE: **350μH MIN @100KHz**
0,1V 8mA DC BIAS

4. DC RESISTANCE: **1,2 OHMS MAX**

5. INSERTION LOSS: **1-100MHz -1,0dB MAX**
100-125MHz -1,2dB MAX

6. RETURN LOSS: **1-40MHz -16dB MIN**
40-60MHz -12dB MIN
60-80MHz -10dB MIN
80-100MHz -8dB MIN

7. CROSS TALK: **1-100MHz -30dB MIN**

8. COMMON TO COMMON MODE ATTENUATION: **1-100MHz -30dB MIN**

9. ISOLATION: PHY SIDE TO LINE SIDE: 2250VDC

- EN ISO 291 23/50

 DIN A3		document-no. / ri 01759664 / 00		article-no. 1664025		scale 2:1		- Simplified representation - Document excluded from change process - Linear dimensions [mm]	
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		description CUC-IB8G-J1ST-AT/R4T							
page 2		of 2							